INFORMATION DISCLOSURE CITATION IN AN APPLICATION (Use several sheets if necessary)			Docket Number (Optional) 81790.0219		Application Number Not Assigned	
			Applicant Katsushi NAGABA and Shigeo OHSHIMA			
			Filing Date September 25, 2001		Group Art Unit Not Assigned	
		U.S. PATENT	DOCUMENTS			<u> </u>
EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
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	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION (YES/NO)
	OTHER DOCUMENTS	(Including Aut	hor, Title, Date, Pertin	ent Pages, E	Etc.)	
₽ H	H.B. Bakoglu "Circuits, Interconnections, and Packaging for VLSI"					
900 "0	CMOS" pages 119 - 129					
EXAMINER		ATE CONSIDER	7/10			
EXAMINER: Initial conformance and	I if citation considered, whether or no not considered. Include copy of this	ot citation is in co s form with next o	onformance with MPEP communication to the ap	§ 609; Draw	line through cita	ation if not in